buffer RAM **216** is temporarily stopped for a time of the read signal R; however, since the time of the write signal W is sufficiently long, the writing the data into the buffer RAM **116** is not hindered by the reading of the data. Accordingly, the character data of the blocks can be surely written in the buffer RAM **116**. Furthermore, in order to make the buffer RAM **116** possible to store the character data applied from the data selector **138** during the process of the NMI interrupt, the buffer RAM **116** has a storage capacity of 4 blocks as described above.

The character data is stored in the character RAM as shown in FIG. **20**, and the color data is stored in the CGRAM, and therefore, the data of the character having the color data is outputted by the PPU (FIG. **10**), and the same is encoded by the video encoder **218**, whereby the color video signal is outputted from the game machine **20**.

Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

What is claimed is:

1. A display information conversion apparatus for making a storage medium which stores a first program executed by a first information processing apparatus which is constituted to display a video image on a dot-matrix display to be applied to a second information processing apparatus which is constituted to display a video image on a raster-scan display, comprising:

a first connection portion to which said storage medium is detachably attached;

a second connection portion for detachably attaching said display information conversion apparatus to said second information processing apparatus;

an information processor for executing said first program of said storage medium being attached to said first connection portion to generate first video image information for the dot-matrix display;

a video image converter for converting said first video image information into second video image information for the raster-scan display; and

transfer circuitry for transferring said second video image information converted by said video image converter to said second information processing apparatus via said second connection portion.

2. An image information conversion apparatus according to claim **1**, wherein said first video image information includes bit-serial data, and said video image converter includes a serial/parallel converter for converting said bit-serial data into bit-parallel data.

3. A video information conversion apparatus according to claim **2**, wherein said video image converter includes a buffer memory and write circuitry for writing said bit-parallel data into said buffer memory, and said transfer circuitry includes read circuitry for reading said bit-parallel data from said buffer memory, and said second information processing apparatus converts said bit-parallel data into a video signal for said raster-scan display.

4. A display information conversion apparatus according to claim **1**, wherein said first information processing apparatus includes information processing means for executing said first program, and said information processor in said display information conversion apparatus is the same as information processing means provided in said first information processing apparatus.

5. A display information conversion apparatus according to claim **1**, further comprising operation means provided in association with said second information processing apparatus, and operation information converting means for converting operation information from said operation means into operation information adapted to said information processor.

6. A display processing system, comprising:

a storage medium for storing a program executed by first information processing apparatus which is constituted to display a video image on a dot-matrix display;

a second information processing apparatus constituted to display a video image on a raster-scan display;

an information processor for executing said first program of said storage medium to generate first video image information for said dot-matrix display;

a video converter for converting said first video image information into second video image information for said raster-scan display; and

transfer circuitry for transferring said second video information converted by said video converter to said second information processing apparatus.

7. A video information conversion apparatus for making a memory including a game program for a first game machine which displays a game video image on a dot-matrix display to be applied to a second game machine which displays a game video image on a raster-scan display, comprising:

a first connection portion to which said memory is detachably attached;

a second connection portion for detachably attaching said display information conversion apparatus to said second game machine;

a game processor for executing said game program of said memory attached to said first connection portion to generate dot data for said dot-matrix display; and

a data converter for converting said dot data into character data for said raster-scan display; and

transfer circuitry for transferring said character data converted by said data converter to said second game machine through said second connection portion.

8. A display information conversion apparatus for making a storage medium which stores in a non-volatile fashion a first program executed by a first information processing apparatus which is constituted to display a video image on the basis of a first specification to be applied to a second information processing apparatus which is constituted to display a video image on the basis of a second specification, comprising:

a first connection portion to which said storage medium is detachably attached;

a second connection portion for detachably attaching said display information conversion apparatus to said second information processing apparatus;

an information processor for executing said first program of said storage medium being attached to said first connection portion to generate first video image information for said first specification;

a video image converter for converting said first video image information into second video image information for said second specification; and

transfer circuitry for transferring said second video image information converted by said video image converter to said second information processing apparatus via said second connection portion.